

# SMD Type Crystal Oscillators

## DS0321SV



Actual size

### Features

- Micro-miniature SMD-SPXO with size 3225 and thickness 1.1mm.
- Tri-state function.
- 3.3V drive and low current consumption.

### Applications

- AP processor clock signal source for mobile phones, PDA, DSC and other mobile equipment.

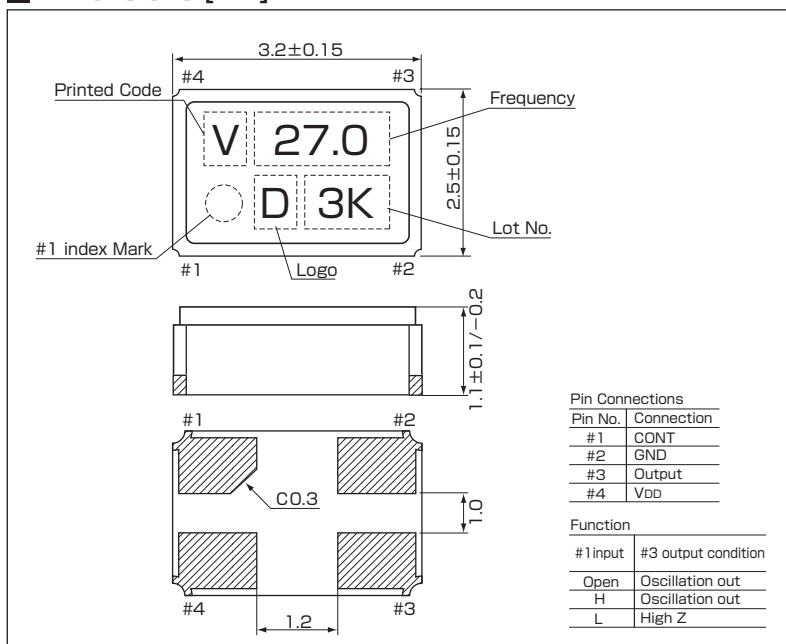
### Generic Specification

Item	Type	Legend	DS0321SV	Condition
Generic Characteristics	Output Frequency Range	Fo	0.700~80MHz	
	Frequency Stability	$\Delta f/Fo$	$\pm 50 \times 10^{-6}$ , $\pm 100 \times 10^{-6}$	-10~+70°C, Includes frequency tolerance at room temperature.
	Supply Voltage	VDD	+3.3V $\pm$ 0.3V	
	Operating Temperature Range	TOPR	-10°C~+70°C	
	Current Consumption 1	IDD1	2mA max. (Fo $\leq$ 32MHz), 3mA max. (32<Fo $\leq$ 60MHz), 4mA max. (60<Fo $\leq$ 80MHz)	When no load is applied.
	Current Consumption 2 (1pin, "L" Level)	IDD2	0.005mA max.	High impedance output/Oscillation stop
Output Characteristics	Output Disable Time	TPLZ	150nsec. max.	
	Output Enable Time	TPZL	5msec. max.	
	Symmetry of Wave Form	Symmetry	45~55% (Fo $\leq$ 10MHz) 40~60% (Fo>10MHz)	
	Rise and Fall Time	Tr, Tf	10nsec. max. (Fo $\leq$ 50MHz), 7nsec. max. (50<Fo $\leq$ 67MHz), 5nsec. max. (Fo>67MHz)	VDD $\times$ 0.1~VDD $\times$ 0.9V
	"0" Level	VOL	VDD $\times$ 0.1 max.	
	"1" Level	VOH	VDD $\times$ 0.9 min.	
	Output Load	CL	15pF max.	
Input Characteristics	Input Voltage Level	VIL VIH	VIL=VDD $\times$ 0.2 max. VIH=VDD $\times$ 0.8 min.	
	Input Current	IIL IIH	IIL=-100 $\mu$ A max. IIH=+100 $\mu$ A max.	

Packing Unit : 2000pcs./reel (  $\phi$  180)

Consult our sales representative for other specifications.

### Dimensions [mm]



### Recommended Land Pattern [mm]

