TOSHIBA Field Effect Transistor Silicon N Channel MOS Type (DTMOS II)

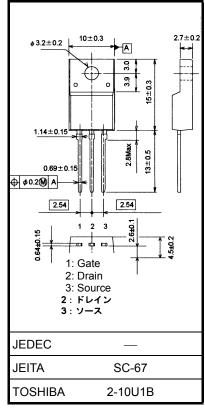
# **TK12A60U**

#### Switching Regulator Applications

- Low drain-source ON-resistance :  $R_{DS (ON)} = 0.36 \Omega$  (typ.)
- High forward transfer admittance  $: |Y_{fs}| = 7.0 \text{ S (typ.)}$
- Low leakage current: I<sub>DSS</sub> = 100 μA (V<sub>DS</sub> = 600 V)
- Enhancement-mode:  $V_{th}$  = 3.0 to 5.0 V ( $V_{DS}$  = 10 V,  $I_D$  = 1 mA)

#### Absolute Maximum Ratings (Ta = 25°C)

Characteristics		Symbol	Rating	Unit	
Drain-source voltage		V <sub>DSS</sub>	600	V	
Drain-gate voltage ( $R_{GS} = 20 \text{ k}\Omega$ )		V <sub>DGR</sub>	600	V	
Gate-source voltage		V <sub>GSS</sub>	±30	V	
Drain current	DC (Note 1)	۱ <sub>D</sub>	12		
	Pulse (t = 1 ms) (Note 1)	I <sub>DP</sub>	24	A	
Drain power dissipati	on (Tc = 25°C)	PD	35	W	
Single pulse avalanche energy (Note 2)		E <sub>AS</sub>	69	mJ	
Avalanche current (Note 3)		I <sub>AR</sub>	12	А	
Repetitive avalanche energy		E <sub>AR</sub>	3.5	mJ	
Channel temperature		T <sub>ch</sub>	150	°C	
Storage temperature range		T <sub>stg</sub>	-55 to 150	°C	



Weight : 1.7 g (typ.)

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings. Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

#### Thermal Characteristics

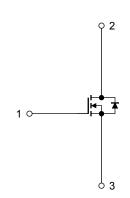
Characteristics	Symbol	Max	Unit
Thermal resistance, channel to case	R <sub>th (ch-c)</sub>	3.57	°C/W
Thermal resistance, channel to ambient	R <sub>th (ch-a)</sub>	62.5	°C/W

Note 1: Ensure that the channel temperature does not exceed 150°C.

Note 2:  $V_{DD}$  = 90 V,  $T_{ch}$  = 25°C (initial), L = 0.84 mH,  $R_G$  = 25  $\Omega$ ,  $I_{AR}$  = 12 A

Note 3: Repetitive rating: pulse width limited by maximum channel temperature

This transistor is an electrostatic-sensitive device. Handle with care.



Unit: mm

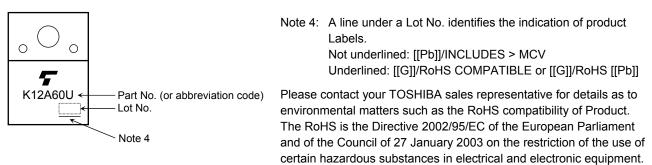
Electrical Characteristics (Ta = 25°C)

Char	acteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Gate leakage cu	rrent	I <sub>GSS</sub>	$V_{GS}=\pm 30~V,~V_{DS}=0~V$	_	_	±1	μA
Drain cut-off current		IDSS	$V_{DS} = 600 \text{ V}, \text{ V}_{GS} = 0 \text{ V}$		_	100	μA
Drain-source bre	akdown voltage	V (BR) DSS	$I_D = 10 \text{ mA}, V_{GS} = 0 \text{ V}$	600	_		V
Gate threshold v	oltage	V <sub>th</sub>	$V_{DS} = 10 \text{ V}, \text{ I}_{D} = 1 \text{ mA}$	3.0	_	5.0	V
Drain-source ON	l-resistance	R <sub>DS (ON)</sub>	$V_{GS} = 10 \text{ V}, \text{ I}_{D} = 6 \text{ A}$		0.36	0.4	Ω
Forward transfer	admittance	Y <sub>fS</sub>	$V_{DS} = 10 \text{ V}, \text{ I}_{D} = 6 \text{ A}$	2.0	7.0		S
Input capacitance		C <sub>iss</sub>			720		
Reverse transfer capacitance		C <sub>rss</sub>	$V_{DS} = 10 \text{ V}, \text{ V}_{GS} = 0 \text{ V}, \text{ f} = 1 \text{ MHz}$		55		pF
Output capacitance		C <sub>oss</sub>			1700		
Switching time	Rise time	tr	$V_{GS}$ $U_{D} = 6 \text{ A}$ $V_{OUT}$		30		
	Turn-on time	t <sub>on</sub>	$\begin{array}{c} & & & \\ & & & & \\ & & & \\ & & & \\ & & & \\ & & & \\ & & & \\ & & & \\ & & & \\ & & & \\ & & & \\ & & & \\ & & & \\ & & & & \\ & & & \\ & & & & \\ & & & \\ & & & & \\ & & & & \\ & & & & \\ & &$		60		ns
	Fall time	t <sub>f</sub>			8		. 113
	Turn-off time	t <sub>off</sub>			75		
Total gate charge		Qg			14	_	
Gate-source charge		Q <sub>gs</sub>	$V_{DD} \approx$ 400 V, $V_{GS}$ = 10 V, $I_D$ = 12 A		8.5	_	nC
Gate-drain charge		Q <sub>gd</sub>	]	_	5.5	_	

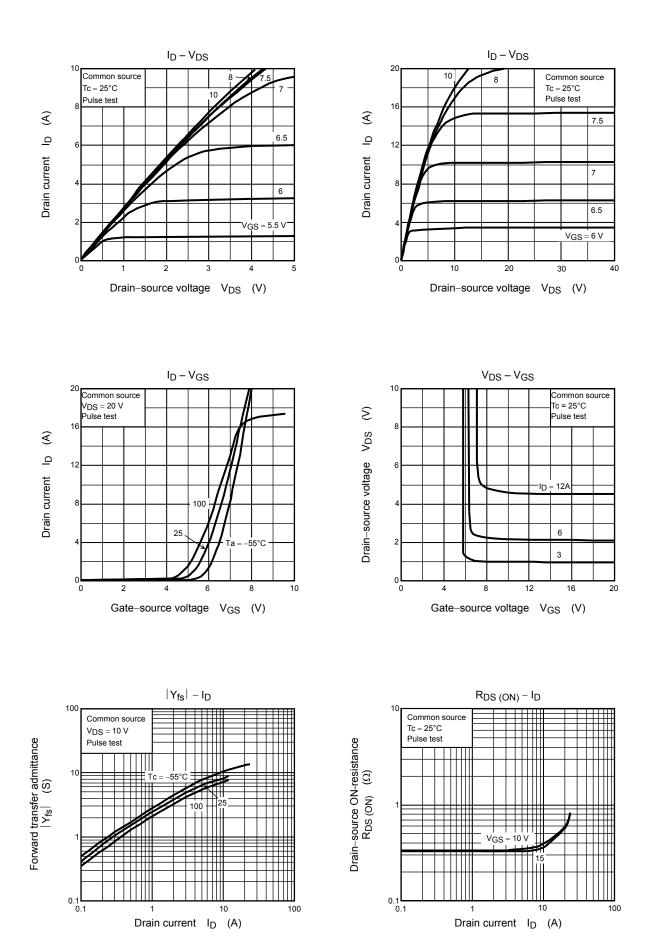
## Source-Drain Ratings and Characteristics (Ta = 25°C)

Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Continuous drain reverse current (Note 1)	I <sub>DR</sub>	—	_	_	12	А
Pulse drain reverse current (Note 1)	I <sub>DRP</sub>	—	_	_	24	А
Forward voltage (diode)	V <sub>DSF</sub>	$I_{DR} = 12$ A, $V_{GS} = 0$ V	_	_	-1.7	V
Reverse recovery time	t <sub>rr</sub>	$I_{DR} = 12 \text{ A}, V_{GS} = 0 \text{ V},$	_	380	_	ns
Reverse recovery charge	Qrr	dI <sub>DR</sub> /dt = 100 A/μs		5.3	_	μC

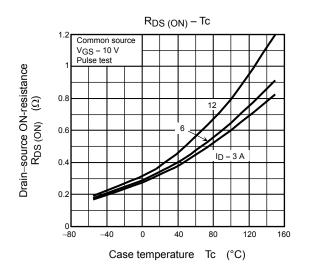
## Marking

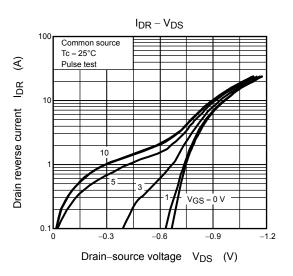


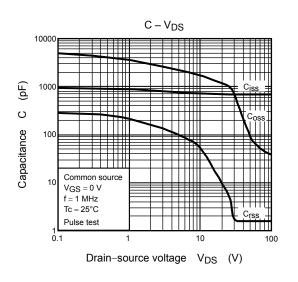
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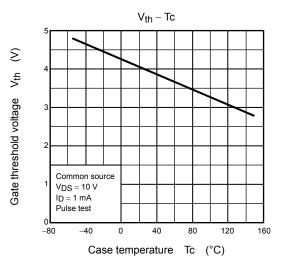


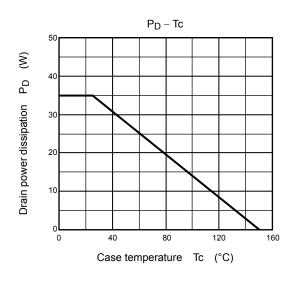
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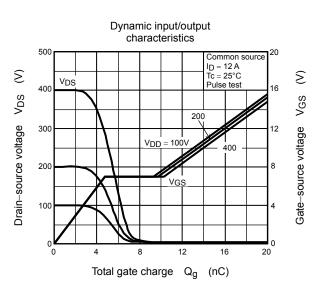


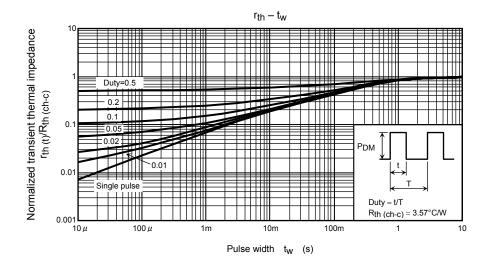


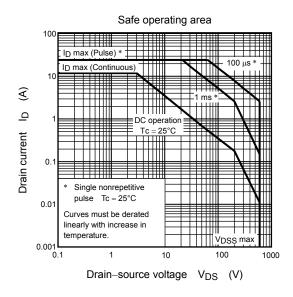


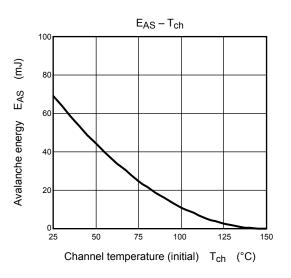


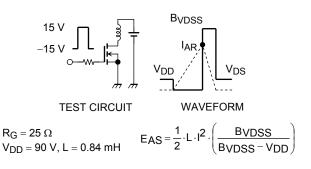












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