

RJH60D3DPE

Silicon N Channel IGBT
Application: Inverter

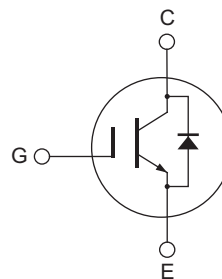
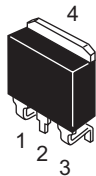
REJ03G1844-0100
Rev.1.00
Oct 14, 2009

Features

- High breakdown-voltage
- Low on-voltage
- Built-in diode

Outline

RENESAS Package code: PRSS0004AE-B
(Package name: LDKPAK (S)-(1))



1. Gate
2. Collector
3. Emitter
4. Collector

Absolute Maximum Ratings

(Ta = 25°C)

| Item | Symbol | Ratings | Unit | |
|--|---------------------------------|-------------|------|---|
| Collector to emitter voltage / diode reverse voltage | V_{CES} / V_R | 600 | V | |
| Gate to emitter voltage | V_{GES} | ±30 | V | |
| Collector peak current | I_C | Tc = 25°C | 30 | A |
| | | Tc = 100°C | 15 | A |
| Collector peak current | $i_{c(peak)}$ ^{Note1} | 60 | A | |
| Collector to emitter diode forward current | i_{DF} | 15 | A | |
| Collector to emitter diode forward peak current | $i_{DF(peak)}$ ^{Note1} | 60 | A | |
| Collector dissipation | P_C ^{Note2} | 120 | W | |
| Junction to case thermal impedance | θ_{j-c} ^{Note2} | 1.04 | °C/W | |
| Junction temperature | T_j | 150 | °C | |
| Storage temperature | T_{stg} | -55 to +150 | °C | |

- Notes: 1. $PW \leq 10 \mu s$, duty cycle $\leq 1\%$
2. Value at Tc = 25°C

Electrical Characteristics

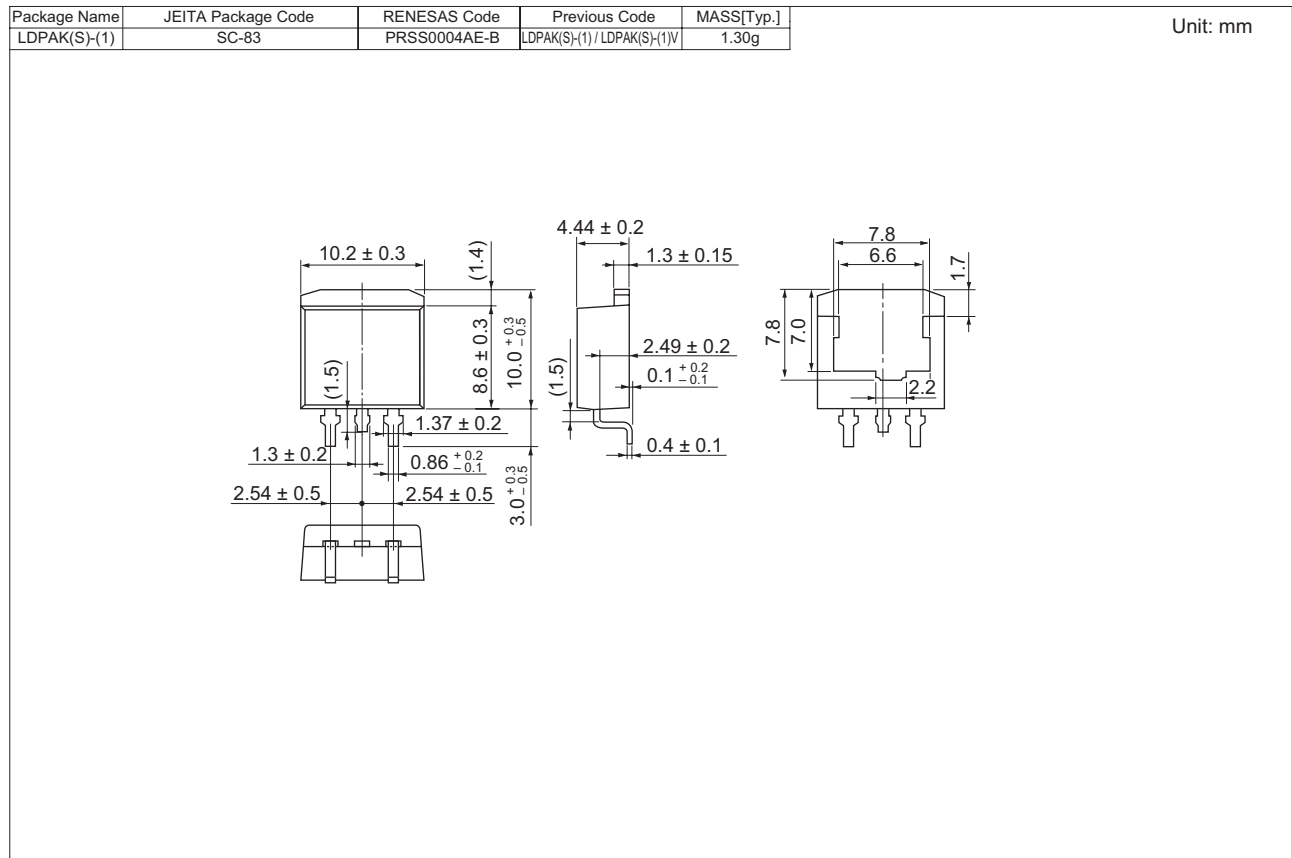
(Ta = 25°C)

| Item | Symbol | Min | Typ | Max | Unit | Test Conditions |
|--|-----------------|-----|-----|---------|---------------|---|
| Zero gate voltage collector current / Diode reverse current | I_{CES} / I_R | — | — | 100 | μA | $V_{CE} = 600 \text{ V}, V_{GE} = 0$ |
| Gate to emitter leak current | I_{GES} | — | — | ± 1 | μA | $V_{GE} = \pm 30 \text{ V}, V_{CE} = 0$ |
| Gate to emitter cutoff voltage | $V_{GE(off)}$ | 4.0 | — | 6.0 | V | $V_{CE} = 10 \text{ V}, I_C = 1 \text{ mA}$ |
| Collector to emitter saturation voltage | $V_{CE(sat)}$ | — | 1.6 | 2.2 | V | $I_C = 15 \text{ A}, V_{GE} = 15 \text{ V}$ ^{Note3} |
| | $V_{CE(sat)}$ | — | 1.8 | — | V | $I_C = 30 \text{ A}, V_{GE} = 15 \text{ V}$ ^{Note3} |
| Input capacitance | C_{ies} | — | 900 | — | pF | $V_{CE} = 25 \text{ V}$ |
| Output capacitance | C_{oes} | — | 50 | — | pF | $V_{GE} = 0$ |
| Revers transfer capacitance | C_{res} | — | 30 | — | pF | $f = 1 \text{ MHz}$ |
| Total gate charge | Q_g | — | 35 | — | nC | $V_{GE} = 15 \text{ V}$ |
| Gate to emitter charge | Q_{ge} | — | 7 | — | nC | $V_{CE} = 300 \text{ V}$ |
| Gate to collector charge | Q_{gc} | — | 20 | — | nC | $I_C = 15 \text{ A}$ |
| Switching time | $t_{d(on)}$ | — | 40 | — | ns | $I_C = 15 \text{ A}$ |
| | t_r | — | 45 | — | ns | $R_L = 20 \Omega$ |
| | $t_{d(off)}$ | — | 60 | — | ns | $V_{GE} = 15 \text{ V}$ |
| | t_f | — | 100 | — | ns | $R_g = 5 \Omega$ |
| FRD Forward voltage | V_F | — | 1.8 | 2.3 | V | $I_F = 15 \text{ A}$ ^{Note3} |
| FRD reverse recovery time | t_{rr} | — | 100 | — | ns | $I_F = 15 \text{ A}$ $di_F/dt = 100 \text{ A}/\mu\text{s}$ |

Notes: 3. Pulse test.

4. Under development — The specifications potentially be changed without notice.

Package Dimension



Ordering Information

| Part No. | Quantity | Shipping Container |
|------------------|----------|--------------------|
| RJH60D3DPE-00-J3 | 1000 pcs | Taping |

Notes:

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