FAIRCHILD

TIP32 Series(TIP32/32A/32B/32C)

Medium Power Linear Switching Applications

Complement to TIP31/31A/31B/31C



1.Base 2.Collector 3.Emitter

TIP32 Series(TIP32/32A/32B/32C)

PNP Epitaxial Silicon Transistor

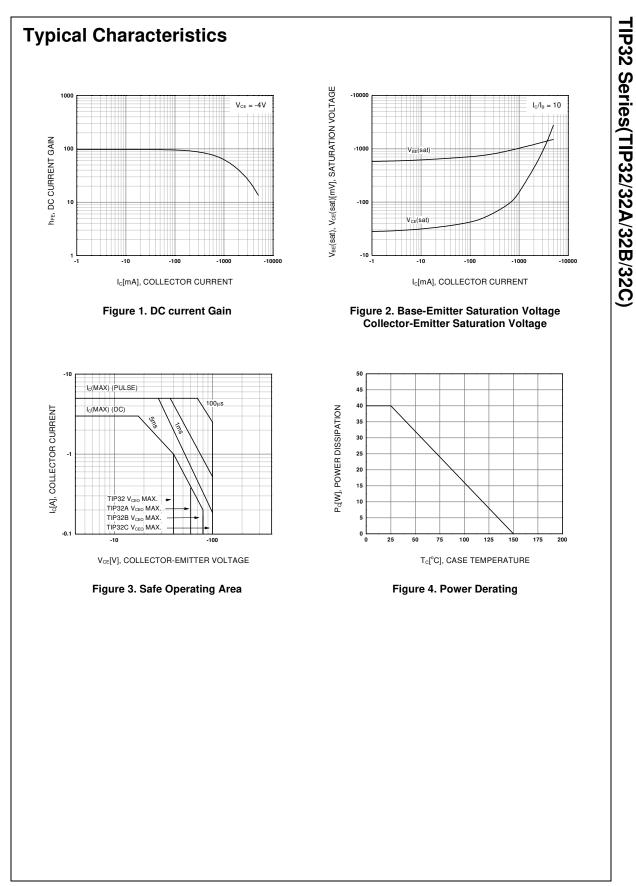
Absolute Maximum Ratings T_C=25°C unless otherwise noted

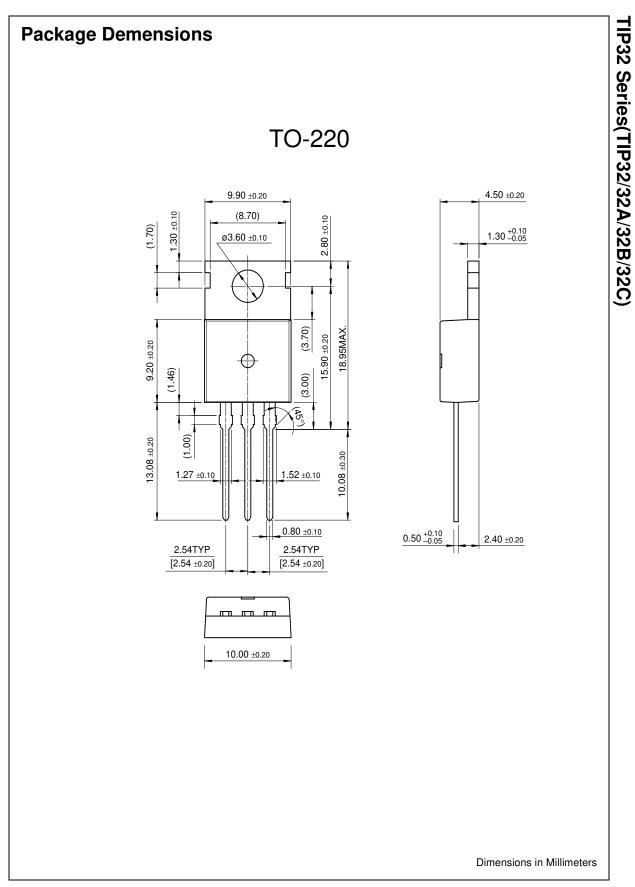
Symbol	Parameter	Value	Units
V _{CBO}	Collector-Base Voltage : TIP32	- 40	V
	: TIP32A	- 60	V
	: TIP32B	- 80	V
	: TIP32C	- 100	V
V _{CEO}	Collector-Emitter Voltage : TIP32	- 40	V
	: TIP32A	- 60	V
	: TIP32B	- 80	V
	: TIP32C	-100	V
V _{EBO}	Emitter-Base Voltage	- 5	V
I _C	Collector Current (DC)	- 3	A
I _{CP}	Collector Current (Pulse)	- 5	A
I _B	Base Current	- 3	A
P _C	Collector Dissipation (T _C =25°C)	40	W
P _C	Collector Dissipation (T _a =25°C)	2	W
TJ	Junction Temperature	150	°C
T _{STG}	Storage Temperature	- 65 ~ 150	°C

Electrical Characteristics T_C=25°C unless otherwise noted

Symbol	Parameter	Test Condition	Min.	Max.	Units
V _{CEO} (sus)	* Collector-Emitter Sustaining Voltage				
	: TIP32	I _C = - 30mA, I _B = 0	-40		V
	: TIP32A		-60		V
	: TIP32B		-80		V
	: TIP32C		-100		V
I _{CEO}	Collector Cut-off Current				
	: TIP32/32A	$V_{CE} = -30V, I_{B} = 0$		- 0.3	mA
	: TIP32B/32C	$V_{CE} = -60V, I_B = 0$		- 0.3	mA
I _{CES}	Collector Cut-off Current				
020	: TIP32	$V_{CE} = -40V, V_{EB} = 0$		- 200	μA
	: TIP32A	$V_{CE} = -60V, V_{EB} = 0$		- 200	μA
	: TIP32B	$V_{CE} = -80V, V_{EB} = 0$		- 200	μΑ
	: TIP32C	$V_{CE} = -100V, V_{CE} = 0$		- 200	μA
I _{EBO}	Emitter Cut-off Current	$V_{EB} = -5V, I_{C} = 0$		- 1	mA
h _{FE}	* DC Current Gain	V _{CE} = - 4V, I _C = - 1A	25		
		$V_{CE} = -4V, I_{C} = -3A$	10	50	
V _{CE} (sat)	* Collector-Emitter Saturation Voltage	I _C = - 3A, I _B = - 375mA		- 1.2	V
V _{BE} (sat)	* Base-Emitter Saturation Voltage	V _{CE} = - 4V, I _C = - 3A		- 1.8	V
f _T	Current Gain Bandwidth Product	V _{CE} = - 10V, I _C = - 500mA	3.0		MHz
* Pulse Test: PW≤3	00μs, Duty Cycle≤2%	· ·	•	•	•

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